



First Named Inventor	Frankie F. Roohparvar	<div>#17/Response 3/31/03 C.P.</div> <div>RESPONSE TO FINAL OFFICE ACTION UNDER 37 U.S.C. § 1.116</div>
Serial No.	09/608,580	
Filing Date	June 30, 2000	
Group Art Unit	2818	
Examiner Name	Trong Q. Phan	
Confirmation No.	9345	
Attorney Docket N ^o .	400.006US01	
Title: ZERO LATENCY-ZERO BUS TURNAROUND SYNCHRONOUS FLASH MEMORY		

AMENDMENT AND RESPONSE UNDER 37 U.S.C. § 1.116

Commissioner for Patents
BOX AF
Washington, D.C. 20231

In response to the Final Office Action mailed January 24, 2003, please consider the following remarks:

REMARKS

Claim Objections

Rejections Under 35 U.S.C. § 112

Claims 2, 19-20 and 22-23 were rejected under 35 U.S.C. § 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Applicant respectfully traverses the rejection.

The Office Action dated January 24, 2003 stated “[t]he features as recited in claims 2, 19-20, and 22-23 are not understood because they are not described in the specification.” The Office Action gave no further explanation of which specific elements in claims 2, 19-20, and 22-23 were considered not to be described in the specification as required under MPEP, Eighth Edition, August 2001, § 706.03(c) and MPEP, Eighth Edition, August 2001, § 706.03(c), Form ¶ 7.31.02, Examiner Note, Item 3.

RECEIVED
MAR 28 2003
TC 2800 MAIL ROOM